

Abstract**Sigma delta modulator**

5 A quantizer for a sigma delta modulator comprising at least one preliminary stage (V1, V2), the quantizer quantizing an input signal (E_Q) present at it in accordance with at least one threshold signal ($y_{th,i}$) and outputting it as a result value (y_Q) at a digital
10 result output (OUT), wherein the quantizer has a number of comparators (S_j) corresponding to the number of threshold signals ($y_{th,j}$), which compare the input signal (E_Q) with the respective threshold signal ($y_{th,j}$), the threshold signal being reduced or increased by a
15 correction voltage (y_{dac3}) or a correction current, the correction voltage or correction current being generated in accordance with the result value (y_Q) output at the result output (OUT).

20 **Figure 6**